

Amendments to the Specification:

In paragraph [0011]:

Fig.2 shows a finite state transition diagram 20 for the bit stream of an RLL(1,7)
5 RMTR = 6 constrained system according to the prior art. All compliant bit transitions are indicated on this diagram. As an example of a non-compliant bit stream, suppose the current state is at node A. Node A indicates the immediately preceding bits received were "...10101010101010" as the only way to reach node A is to pass through this sequence of bits. Node A has only a single path going to node B when a "0" is received. This is
10 because a "0" must be received. If an additional "1" were received, this would mean that the immediately preceding bits were "...101010101010101" ~~"...10101010101010"~~, which violates the RMTR = 6 constraint. Similarly, due to the maximum run of zeros being specified as $k = 7$, node C has only a single path going to node D when a "1" is received. If an additional "0" were received at node C, this would mean that eight zeros
15 were been received in a row, violating the $k = 7$ constraint.